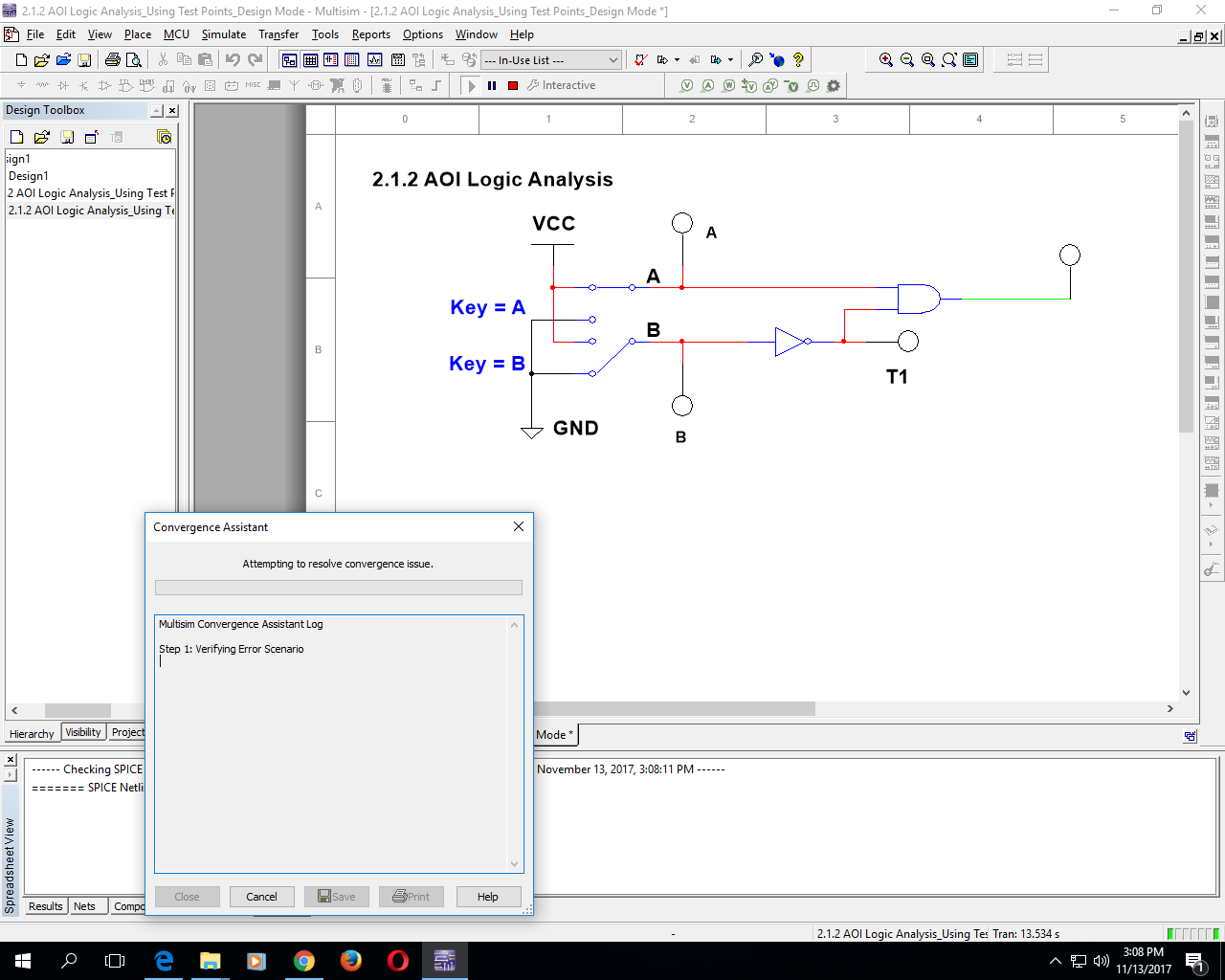
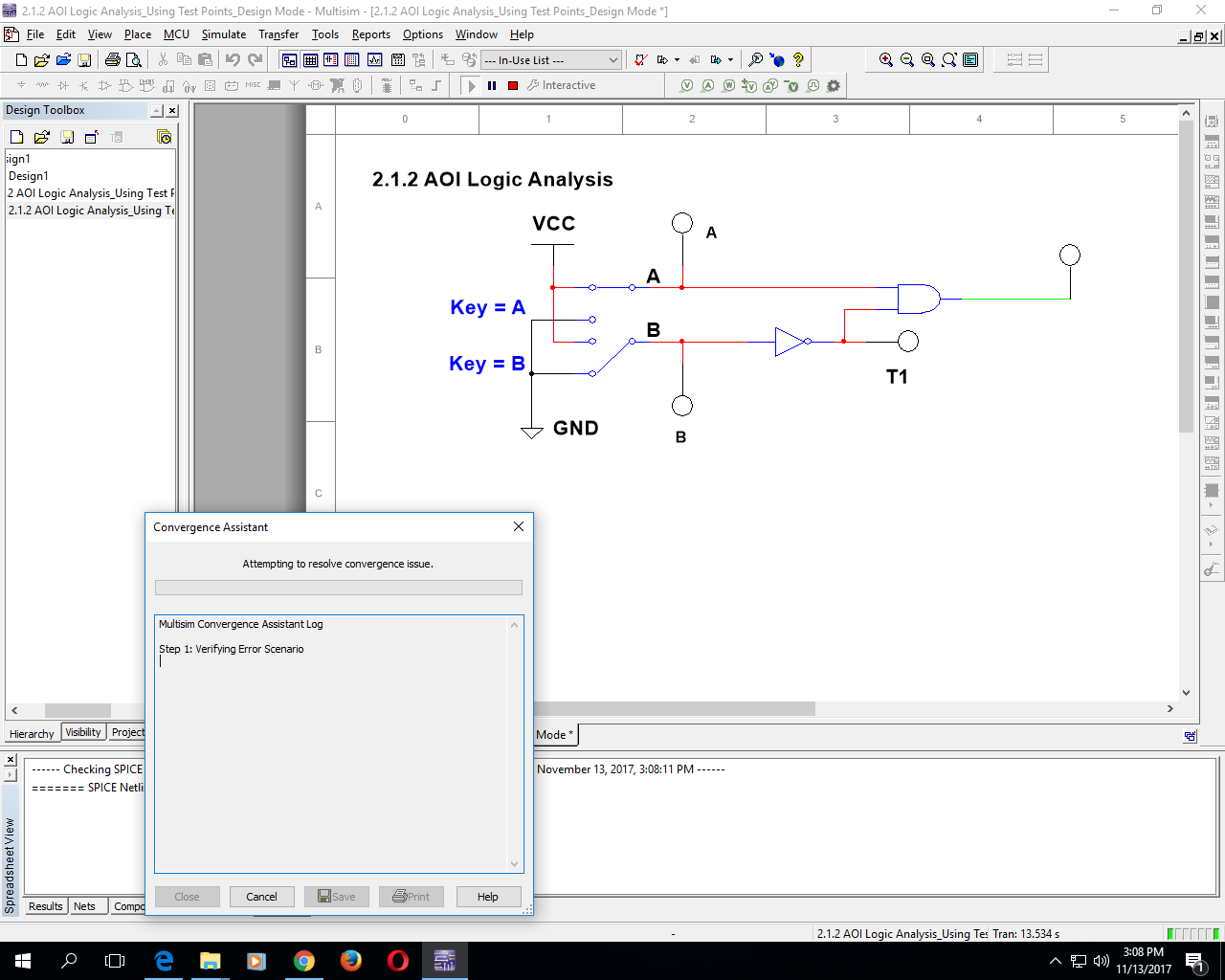
Analyzing logic by \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

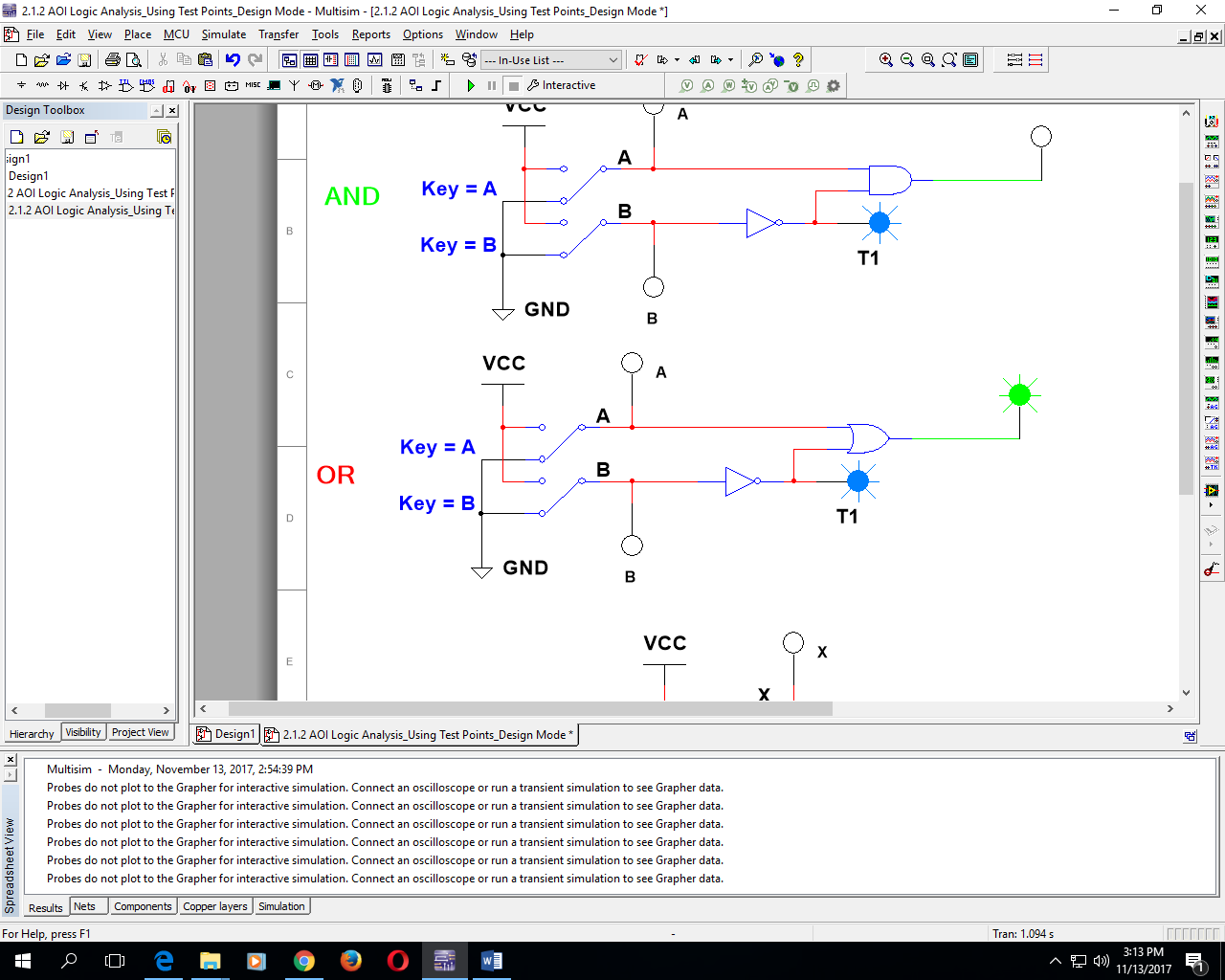
AND

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| --- | --- | --- | --- |
| A | B | T1 | OUTPUT |
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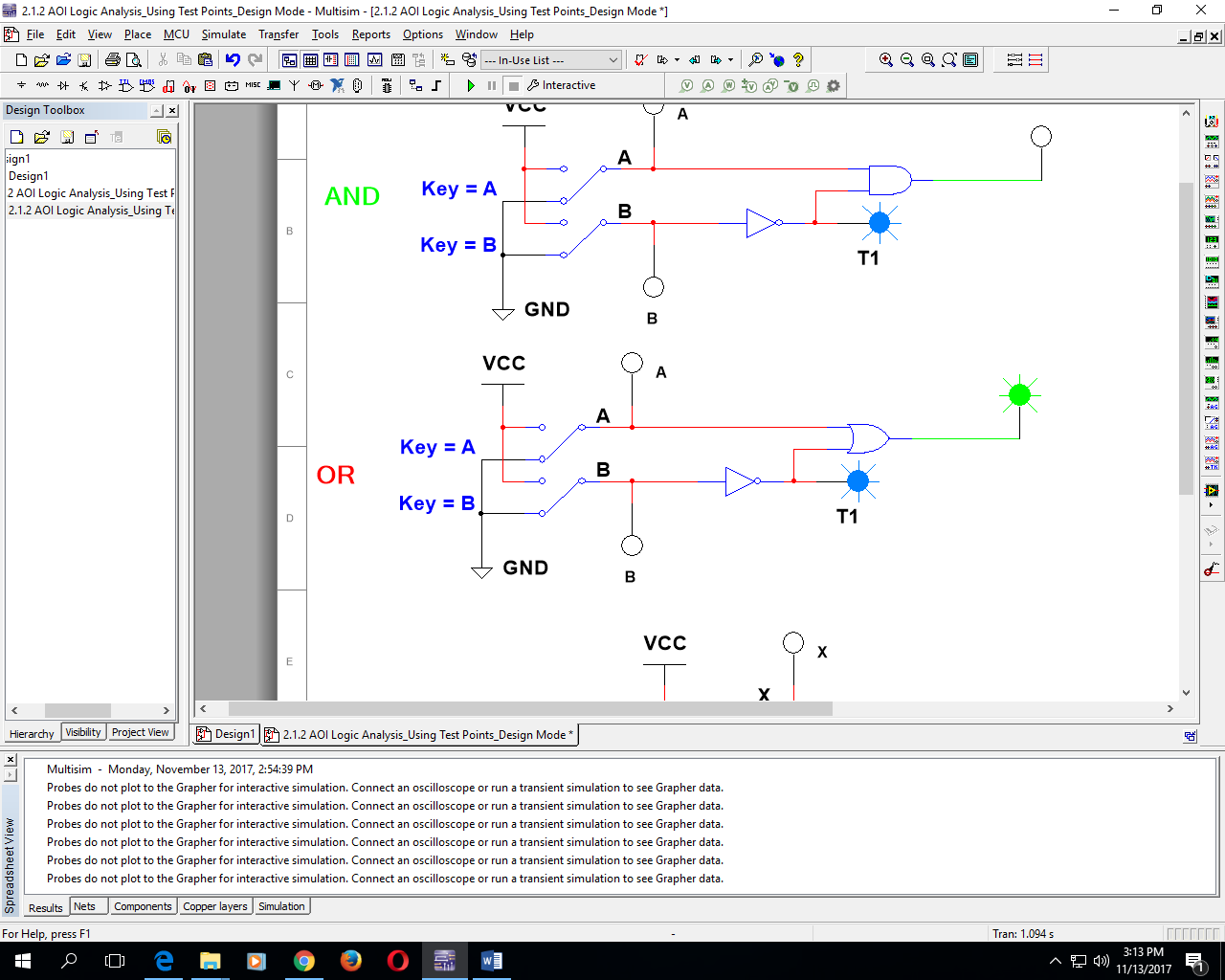
Analyzing logic by \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

AND

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| --- | --- | --- | --- |
| A | B | T1 | OUTPUT |
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| A | B | T1 | OUTPUT |
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| --- | --- | --- | --- |
| A | B | T1 | OUTPUT |
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